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AMENDMENTS TO THE DRAWINGS

The attached 11 sheets of drawings (Figures 1-12) replace the originally filed, informal drawings.

Attachment: Replacement Sheet(s)

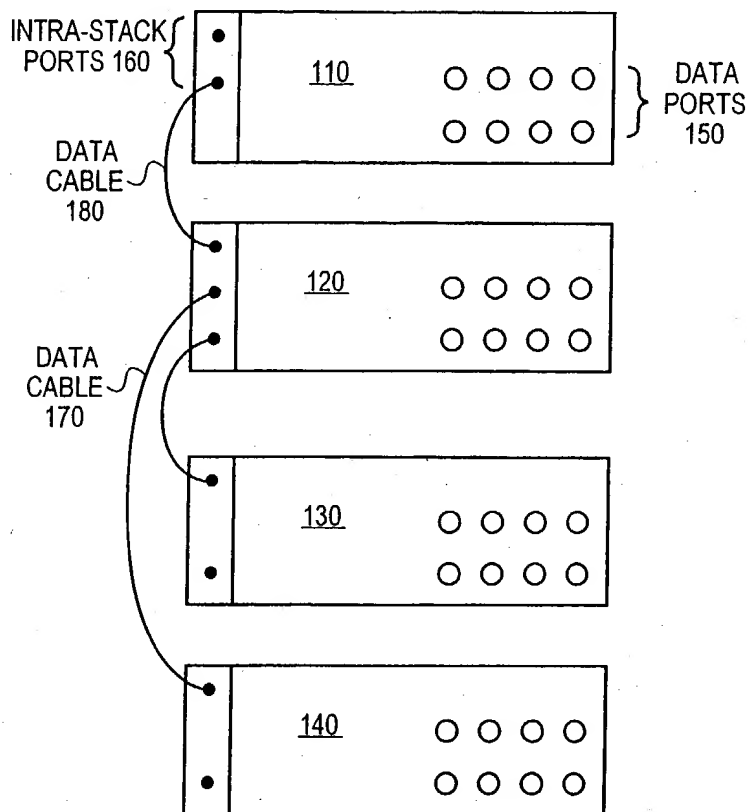


FIG. 1

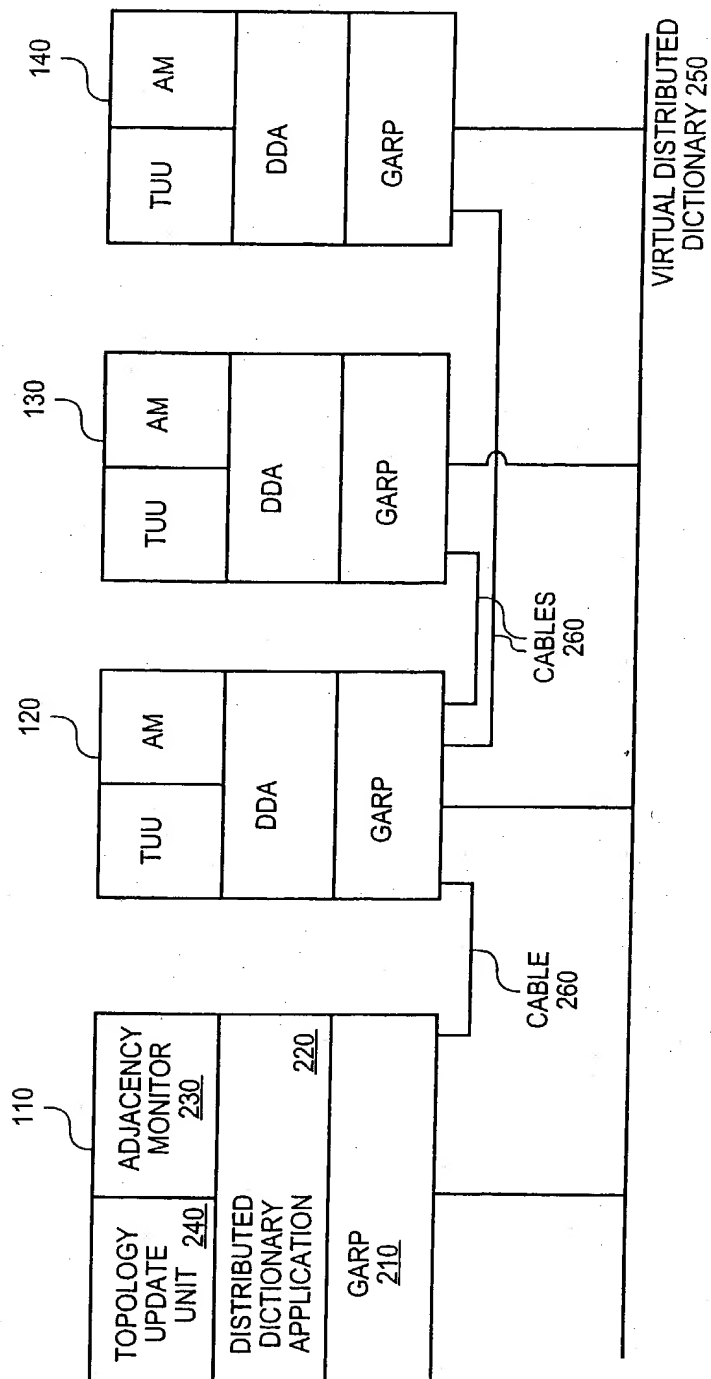
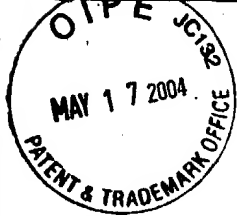


FIG. 2

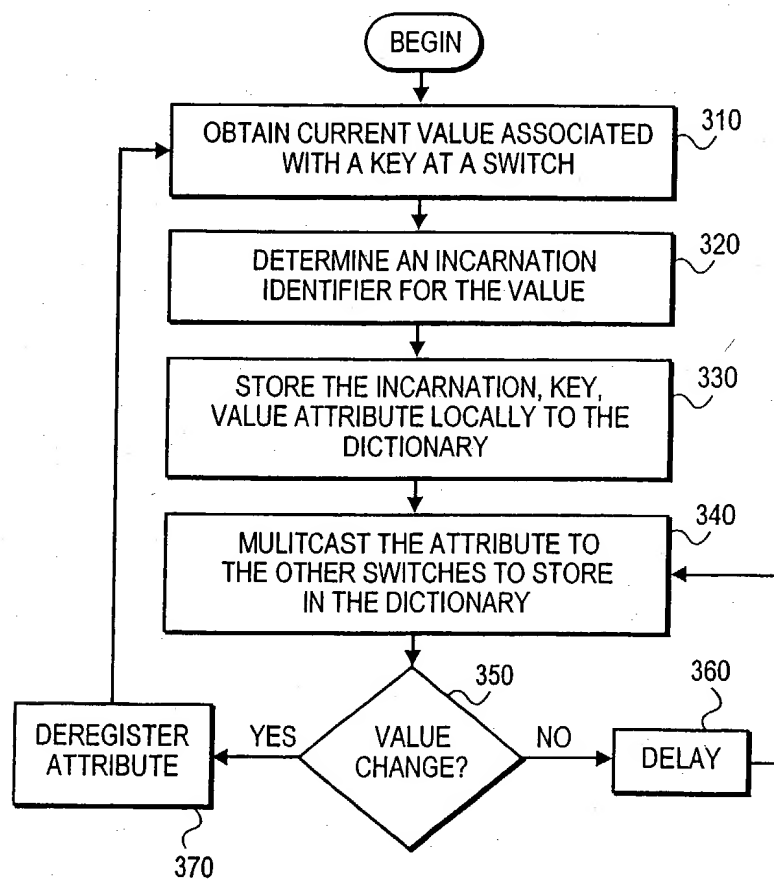


FIG. 3

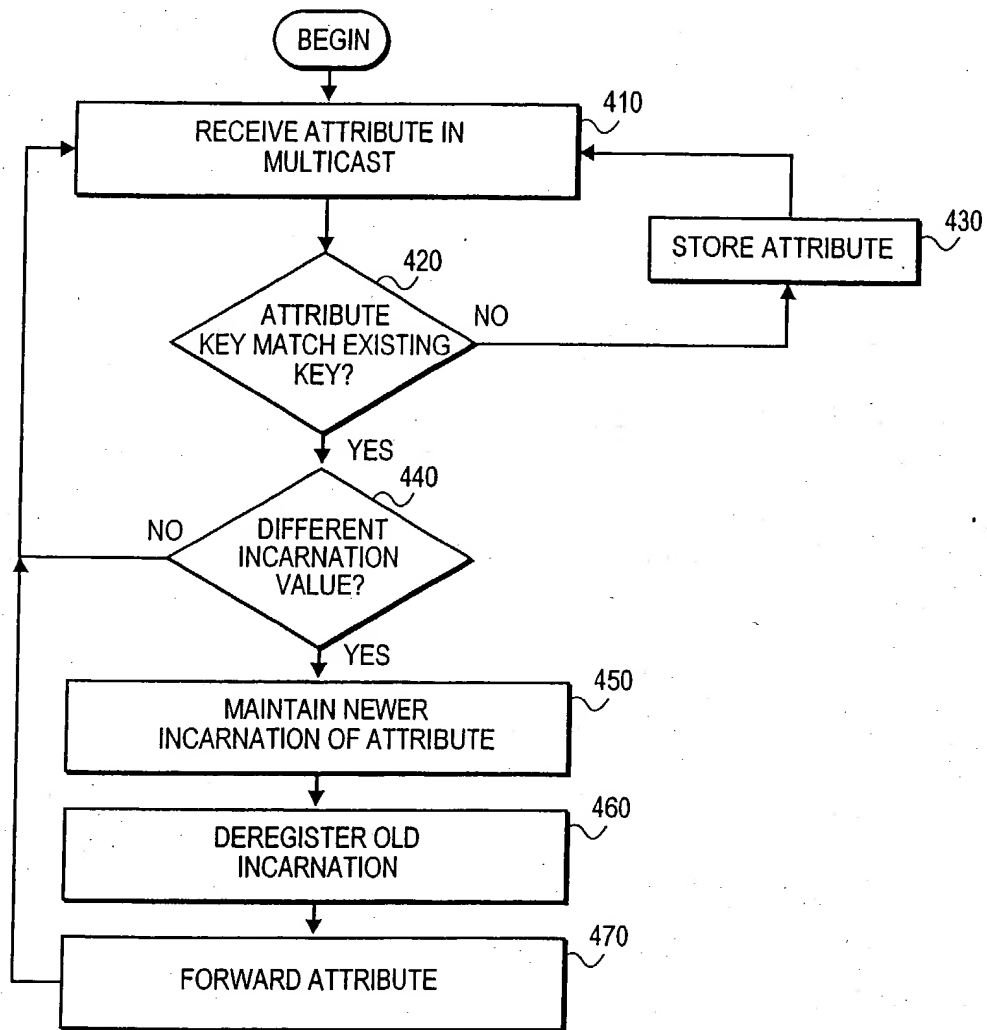


FIG. 4

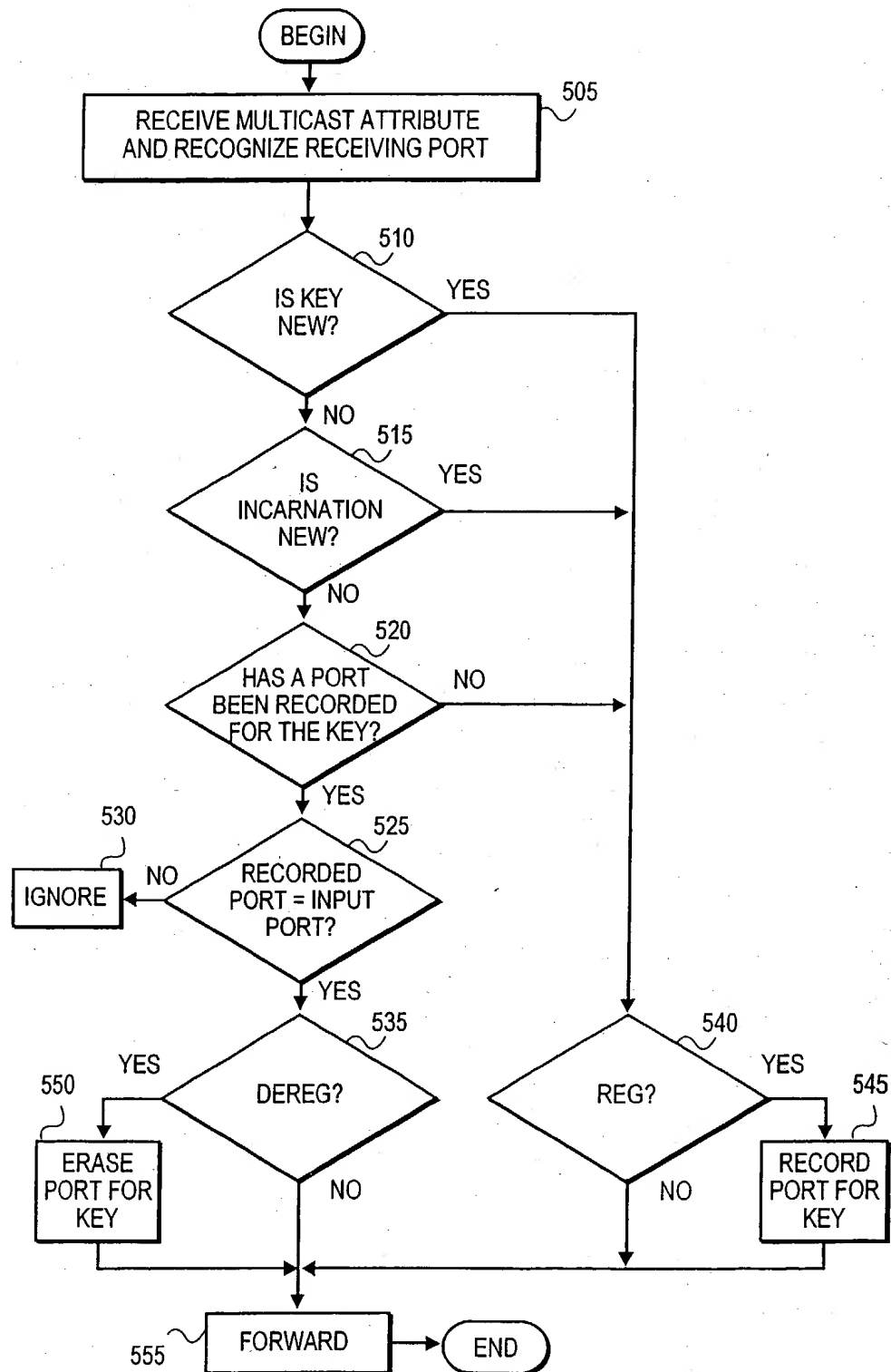


FIG. 5

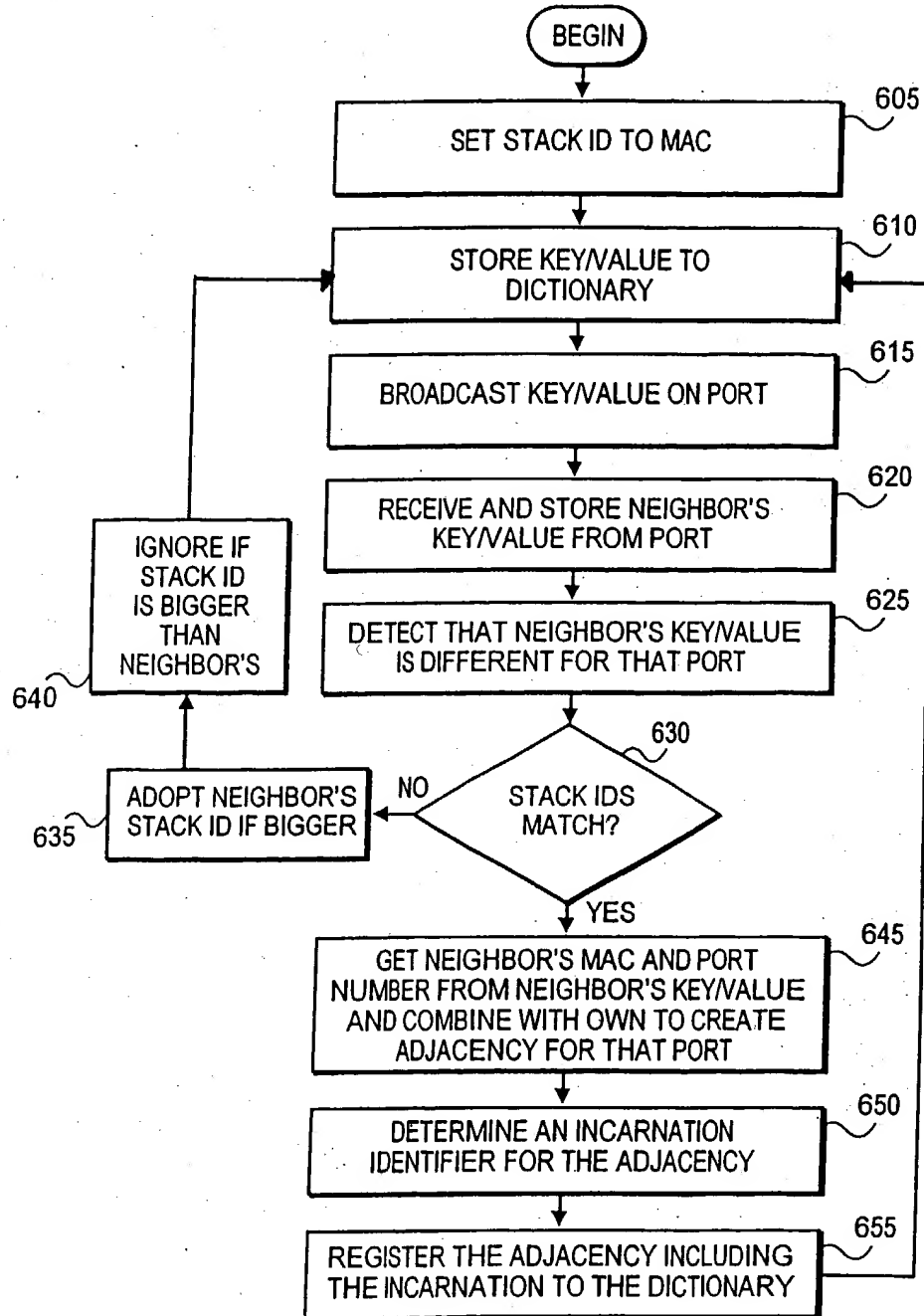
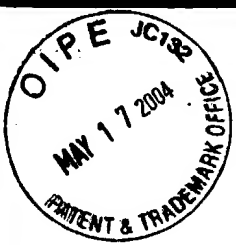


FIG. 6



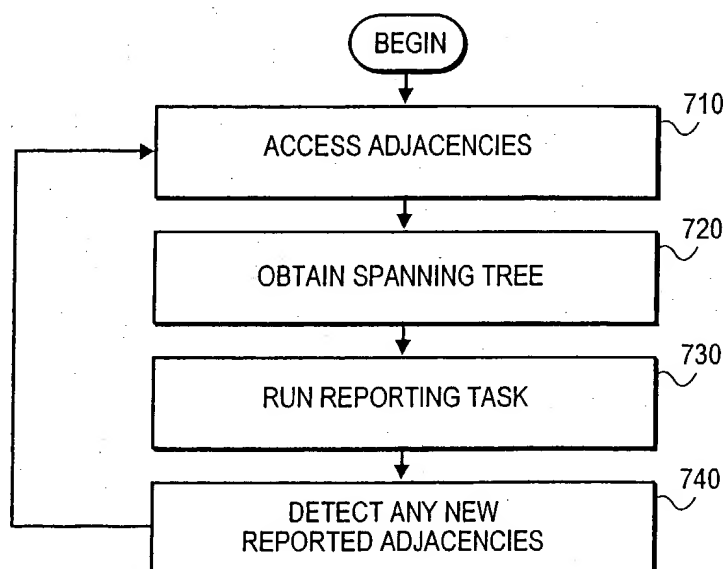
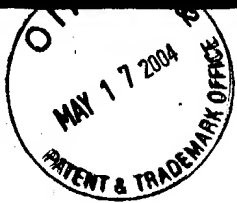
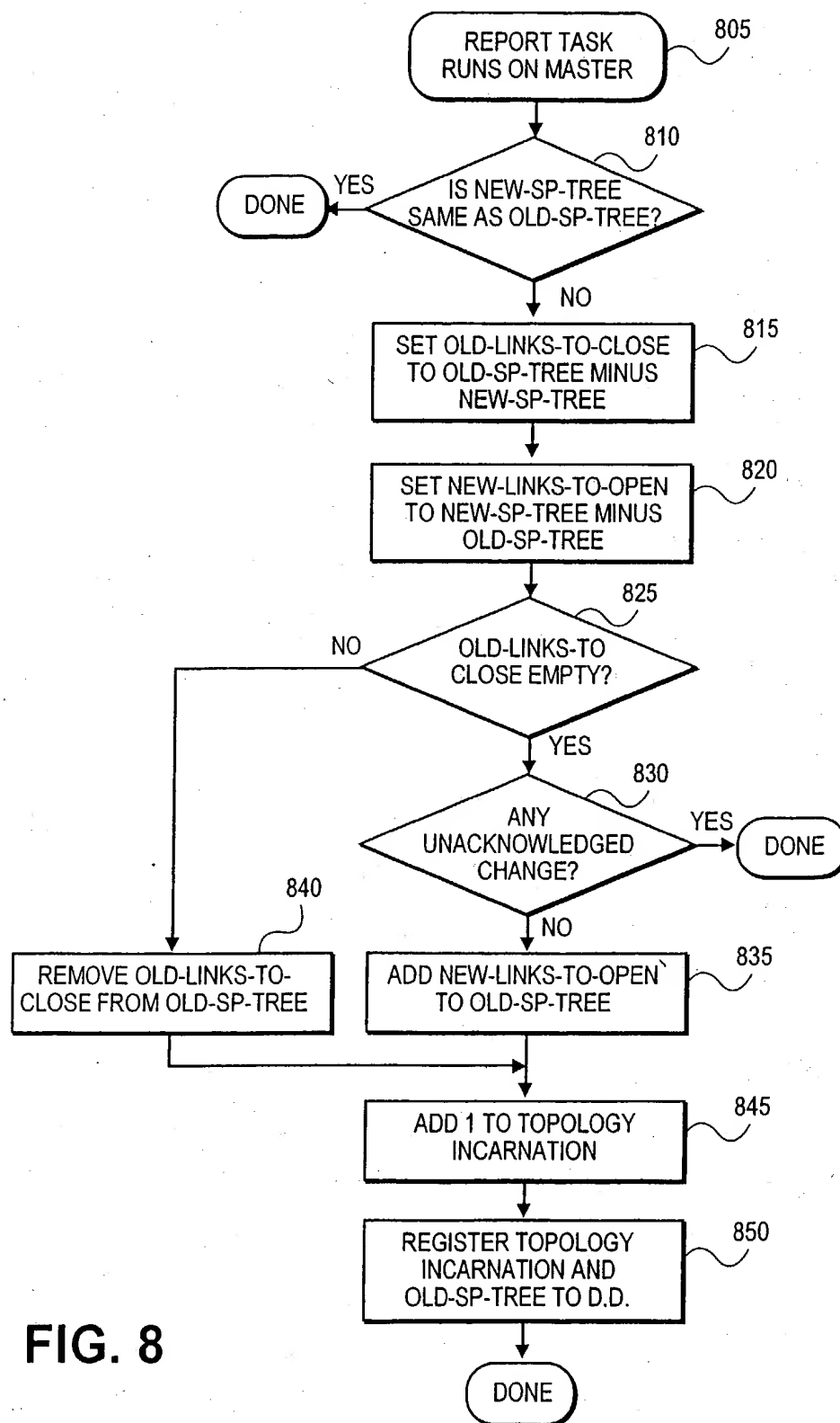
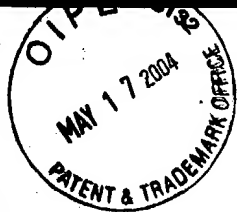
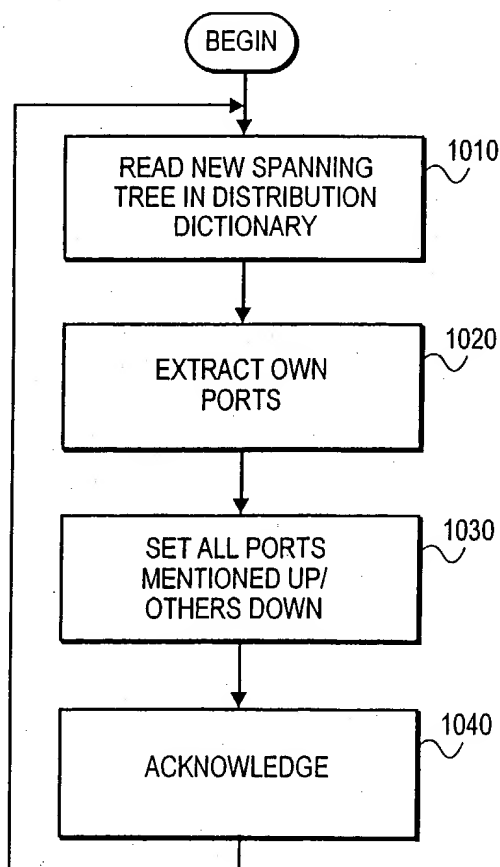
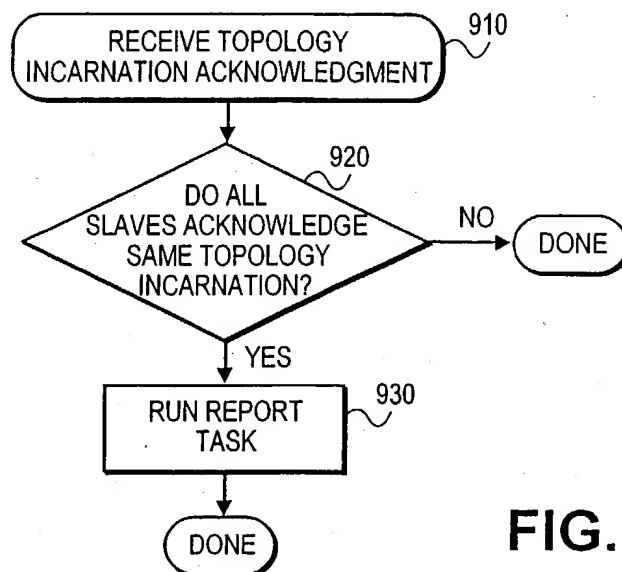
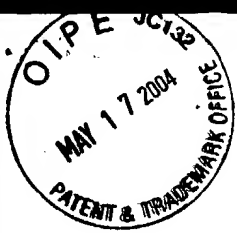


FIG. 7



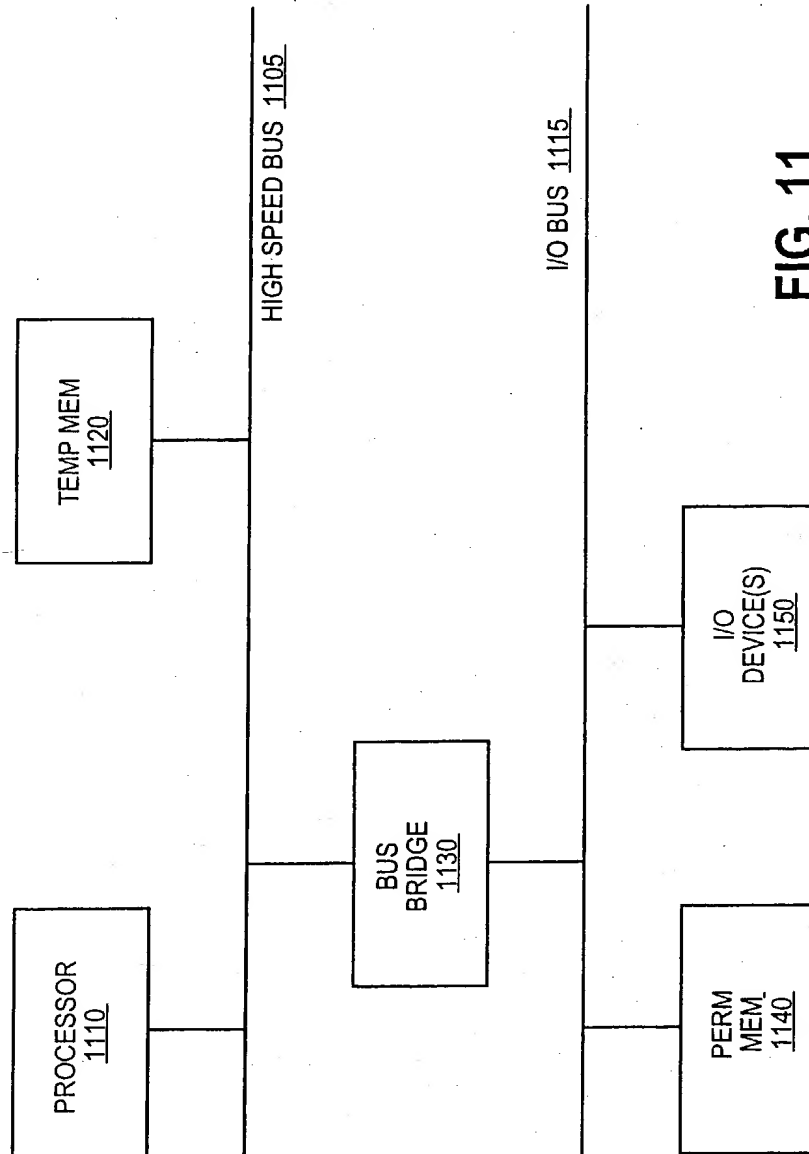




Intel Corporation  
Title METHOD AND APPARATUS FOR AVOIDING TRANSIENT LOOPS DURING  
NETWORK TOPOLOGY ADOPTION  
1<sup>st</sup> Named Inventor: Mikael Sylvest  
Application No.: 09/609,307  
Sheet 10 of 11  
Replacement Sheets

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Docket No.: P8719

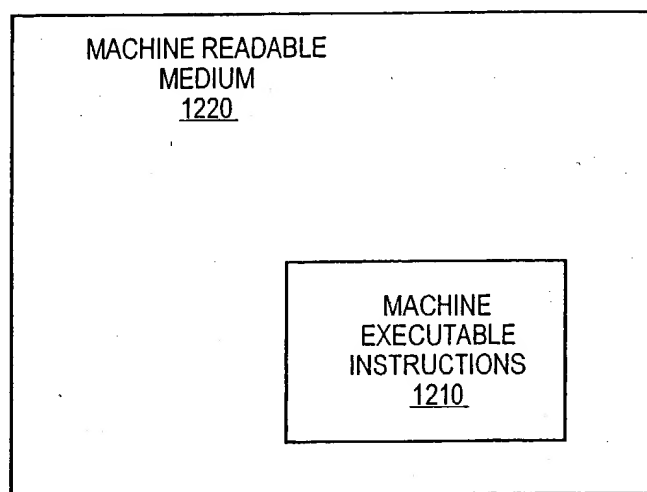


**FIG. 11**



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**FIG. 12**